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\*\* CONTINUING DATA \*\*\*\*\* *none pme*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *yes pme*  
 JAPAN 2003-018920 01/28/2003

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>pme</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 6
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 24998  
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TITLE  
 Semiconductor integrated circuit and scan test method therefor

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